

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination SAKAI ET AL.	
		Examiner John M Ross	Art Unit 2188	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Takashi Fujiwara et al, A Custom Processor for the Multiprocessor System ASCA, 1998, IASTED 16 <sup>th</sup> International Conference on Applied Informatics, pp 258-261
	V	Jim Handy, The Cache Memory Book, 1998, Academic Press Inc., 2 <sup>nd</sup> ed., pp 44-47, 204
	W	Erik G. Hallnor et al, A Fully Associative Software-Managed Cache Design, 2000, ACM Press, pp 107-116
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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